

TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
JP920030017

In Re Application Of: **Nakamura et al.**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/709456	05/06/04	Unassigned	024241		

Title: **DYNAMIC SEMICONDUCTOR MEMORY DEVICE AND BIT LINE PRECHARGE METHOD THEREFOR.**

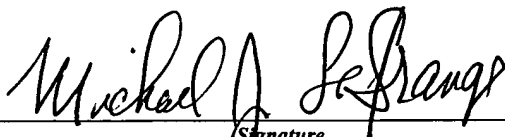
TO THE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE:

Transmitted herewith is:

- 1. Certified copy of foreign patent application (Japanese Application No. JP2003-128367)**
- 2. Return postcard**
3. Information Disclosure Statement
4. PTO-1449

5. Cited Art
in the above identified application.

- ☒ No additional fee is required.
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